ATES PATENT AND TRADEMARK OFFICE

Applicant:

P.H. Mawet

-Attorney-Docket-No.-MEIP1-17200

Application No.: 09/898,674

Group Art Unit: 2857

Filed:

July 3, 2001

Examiner: --

LOW VOLTAGE LOW POWER SIGNAL PROCESSING SYSTEM AND METH FOR HIGH ACCURACY PROCESSING OF DIFFERENTIAL SIGNAL INPU

FROM A LOW POWER MEASURING INSTRUMENT

INFORMATION DISCLOSURE STATEMENT

O THE COMMISSIONER FOR PATENTS:

Applicant is aware of the information listed in the attached form that may be material to the prosecution of the above-identified patent application.

- Copies of the listed patents, publications, and other information are enclosed for the Examiner's use.
- Pursuant to 37 C.F.R. § 1.97(b), this Information Disclosure Statement is being 2. filed within three months of the filing date of the national application (other than a CPA), within three months of the date of entry of the national stage as set forth in 37 C.F.R. § 1.491 in an international application, before the mailing date of a first Office Action on the merits, or before the mailing date of a first Office Action after the filing of an RCE.
- The Commissioner is hereby authorized to charge any fees under 37 C.F.R. 3. §§ 1.16, 1.17 and 1.18 which may be required during the entire pendency of the application, or credit any overpayment, to Deposit Account No. 03-1740. This authorization also hereby includes a request for any extensions of time of the appropriate length required upon the filing of any reply during the entire prosecution of this application. A copy of this document is enclosed.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the U.S. Postal Service in a sealed envelope as first class mail with postage thereon fully prepaid and addressed to the Commissioner for Patents, Washington, D.C. 20231, on the below date.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: P.H. Mawet-

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Attorney Docket No. MEIP117

Application No.: 09/898,674

Filed:

July 3, 2001

Group Art Unit: 2857 LOW VOLTAGE LOW POWER SIGNAL PROCESSING SYSTEM AND METHOD FOR HIGH ACCURACY PROCESSING OF DIFFERENTIAL SIGNAL INPUTS

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